

AN6783S

IC for long interval timer

■ Overview

The AN6783S is an IC designed for a long interval timer. It is oscillated by using the external resistor and capacitor, and the oscillation frequency divided by a 15-stage F.F. is provided as the output.

It is frequency divider type, so that a long interval timer can be constructed by using a capacitor with small capacitance.

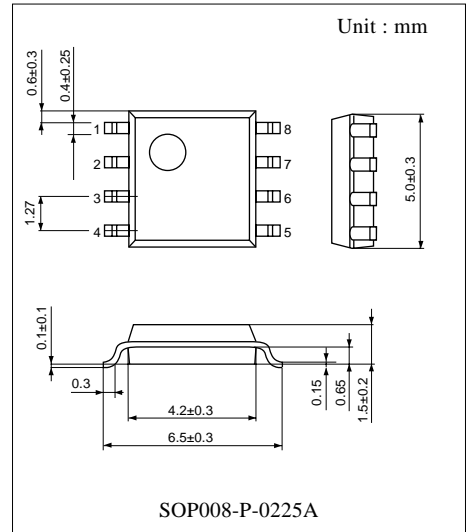
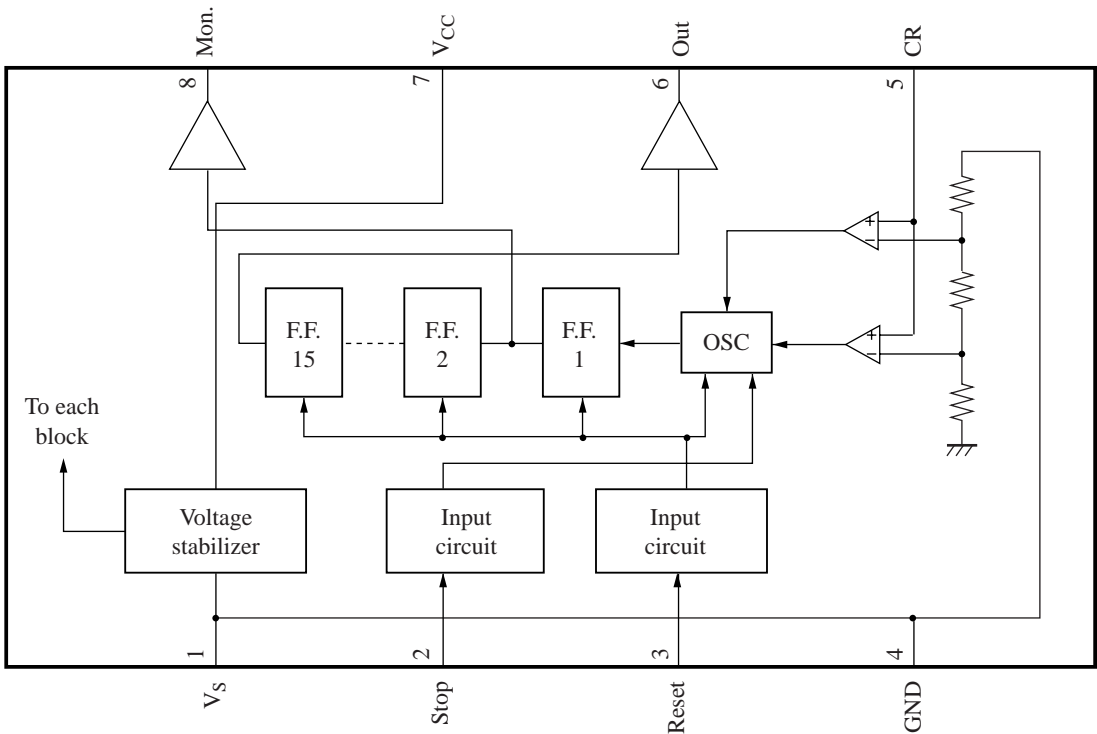
■ Features

- Oscillation frequency dispersion of IC itself is small ($\pm 5\%$)
- Oscillation frequency can be checked by $1/2 f_{OSC}$ monitor terminal
- Wide operating supply voltage range (3.2 V to 18 V)
- Small consumption current (3 mA typ.)

■ Applications

- Timer

■ Block Diagram



■ Pin Descriptions

Pin No.	Symbol	Description
1	V _S	Reference voltage
2	Stop	Oscillation stop input
3	Reset	Reset input
4	GND	Grounding
5	CR	C and R connection
6	Out	Output
7	V _{CC}	Supply voltage
8	Mon.	Monitor pin

■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage	V _{CC}	20	V
Supply current	I _{CC}	7	mA
Power dissipation	P _D	140	mW
Operating ambient temperature *	T _{opr}	-35 to +85	°C
Storage temperature *	T _{stg}	-55 to +125	°C
Stop terminal voltage	V _{STOP}	0 to 18	V
Reset terminal voltage	V _{RESET}	0 to 18	V
CR terminal voltage	V _{CR}	0 to 3	V
Out terminal current	I _{OUT}	-15 to +15	mA

Note) 1. Do not apply external currents or voltages to any pins not specifically mentioned.

For circuit currents, '+' denotes current flowing into the IC, and '-' denotes current flowing out of the IC.

2. *: Except for the operating ambient temperature and storage temperature, all ratings are for T_a = 25°C.

■ Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V _{CC}	3.2 to 18.0	V

■ Electrical Characteristics at T_a = 25°C

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Quiescent supply current	I _{CC}	V _{CC} = 5 V	2.0	4.0	5.0	mA
Internal reference voltage	V _S	V _{CC} = 5 V, I _S = -3 mA	2.40	2.55	2.70	V
High-level input current	I _{IH}	V _{CC} = 18 V, V _{IH} = 18 V	—	—	10	μA
Low-level input current	I _{IL}	V _{CC} = 18 V, V _{IL} = 0 V	-100	—	—	μA
High-level out terminal voltage	V _{OH}	V _{CC} = 18 V, I _{OH} = -10 mA	14.0	16.0	18.0	V
Low-level out terminal voltage	V _{OL}	V _{CC} = 3.2 V, I _{OL} = -10 mA	—	—	0.4	V

■ Electrical Characteristics at $T_a = 25^\circ\text{C}$ (continued)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
High-level Mon. terminal voltage	V_{MH}	$V_{CC} = 18\text{ V}$	17.8	—	18.0	V
Low-level Mon. terminal voltage	V_{ML}	$V_{CC} = 3.2\text{ V}$	—	—	0.4	V
Oscillation frequency precision	f_{OSC}	$V_{CC} = 5\text{ V}$, $R_T = 1\text{ k}\Omega$, $C_T = 0.1\text{ }\mu\text{F}$	9.0	9.5	10.0	kHz
Oscillation frequency fluctuation with supply voltage	Δf_V	$V_{CC} = 5\text{ V}$, 3.2 V to 18.0 V	-5.0	—	5.0	%
High-level input voltage	V_{IH}	$V_{CC} = 5\text{ V}$	2.0	—	—	V
Low-level input voltage	V_{IL}	$V_{CC} = 5\text{ V}$	—	—	0.8	V

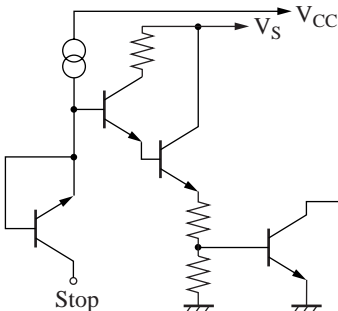
• Design reference data

Note) The characteristic values below are theoretical values for designing and not guaranteed.

The data show the changing amount within $T_a = -35^\circ\text{C}$ to $+85^\circ\text{C}$ when the values at $T_a = 25^\circ\text{C}$ is taken as the reference.

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Oscillation frequency-temperature dependency	Δf_T	$V_{CC} = 5\text{ V}$, $R_T = 1\text{ k}\Omega$, $C_T = 0.1\text{ }\mu\text{F}$	-5.0	—	5.0	%

■ Terminal Equivalent Circuits

Pin No.	Equivalent circuit	Description	I/O	DC voltage
1	—	V_S : Stabilized power supply output terminal. Reference voltage source for oscillation circuit.	O	2.55 V
2		Stop : Oscillation stop input terminal. Only the oscillation circuit stops when this terminal becomes low-level. (F.F. is not cleared) When not used, the terminal should be open or connected to V_{CC} .	I	—

■ Terminal Equivalent Circuits (continued)

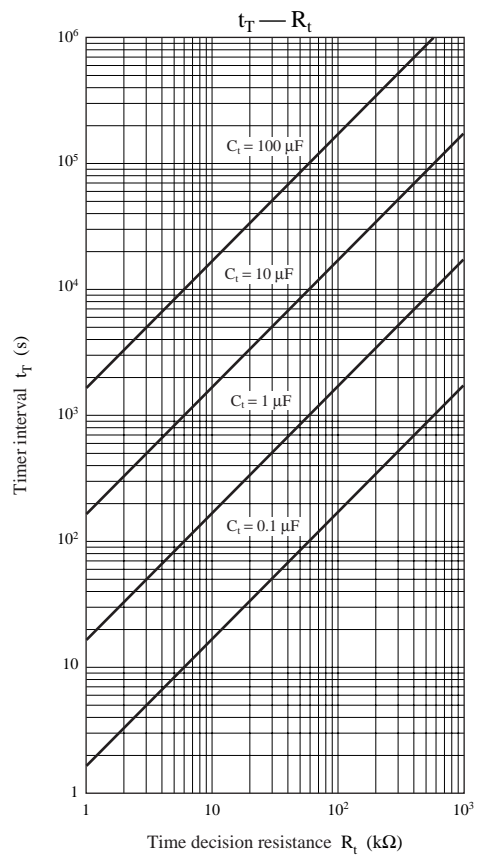
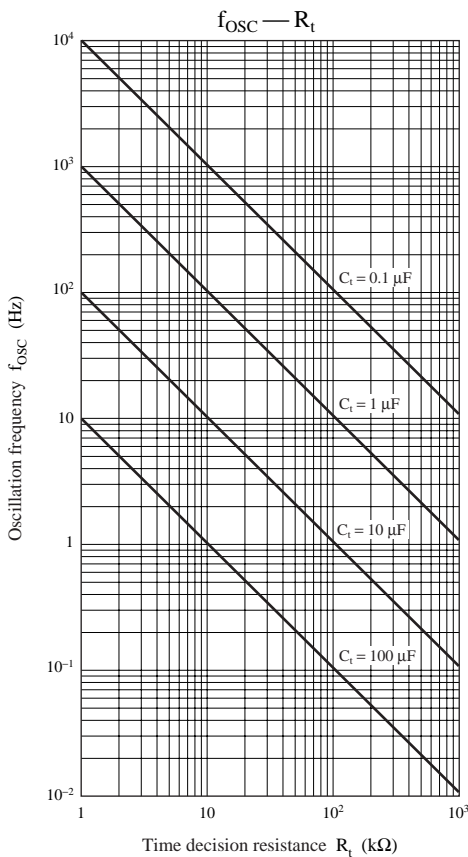
Pin No.	Equivalent circuit	Description	I/O	DC voltage
3		Reset : Reset input terminal. F.F. is reset when the terminal becomes low-level and is set in the initial state. When not used, the terminal should be open or connected to V_{CC} . The reset is applied by rising V_{CC} from a voltage below 0.8 V (power-on reset function).	I	—
4	—	GND : Grounding terminal.	—	0 V
5		CR : C and R connection terminal. The oscillation period is determined by an external resistor and capacitor. When applying a pulse to this terminal from the outside, the voltage should be within the range of 0 V to 3 V.	—	0.7 V to 1.8 V
6		Out : Output terminal. A frequency which is $1/32768$ of the oscillation frequency is outputted. Use with an output current within ± 10 mA.	O	High-level $V_{CC} - 1.4$ V Low-level < 0.4 V
7	—	V_{CC} : Supply voltage terminal.	—	—
8		Mon. : Oscillation frequency monitor terminal. The output is given from the first stage of F.F. and a frequency which is $1/2$ of the oscillation frequency f_{OSC} is outputted. If not used, the terminal should be open. This terminal is provided for connecting probe such as oscilloscope. Use it with an output current under 100 μ A.	O	High-level V_{CC} Low-level < 0.4 V

■ Usage Notes

Pay attention to the following matters in order to prevent the destruction during the use and to increase the reliability ;

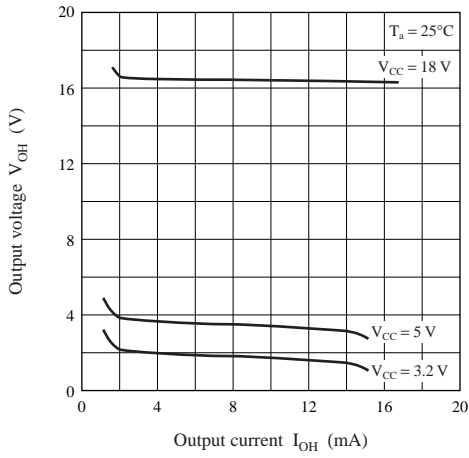
1. In the application circuit example, the calculated value by the theoretical equation for timer interval calculation and the characteristics curve vary depending on the precision of the time interval capacitor used in the actual set. Take such matter of the above into consideration.
Also, the proportional constant α depends on the kinds and characteristics of the time interval resistor and time interval capacitor.
Make a final confirmation with the finished products.
In the case when a high precision is required, adjust the value by using a variable resistor as the time interval resistor.
2. Use a time interval resistor in the range of 1 k Ω to 1 M Ω , and a time interval capacitor in the range of 0.1 μ F to 100 μ F which is polyester or tantalum electrolytic capacitor with a small $\tan\delta$ value.
3. Connect a capacitor (1 μ F to 10 μ F) to the V_S terminal in order to protect the IC from an external noise and stabilize its operation.
4. If turning on power again after an extremely short-time power supply off state during the normal operation, be careful that there may be a case that the automatic reset (power-on reset) fails due to a residual potential of the external capacitance.
5. Take measures against noise in order to avoid the malfunction caused by the external noise. Pay attention to the noise from an external source especially at setting a long interval time.
6. When connecting a plunger or relay to the output circuit, connect a diode to the both ends of coil in order to protect the IC from the counter-electromotive force generated after the circuit is turned off .
7. Do not short circuit the V_S terminal with V_{CC} terminal in order to avoid malfunction.

■ Main Characteristics

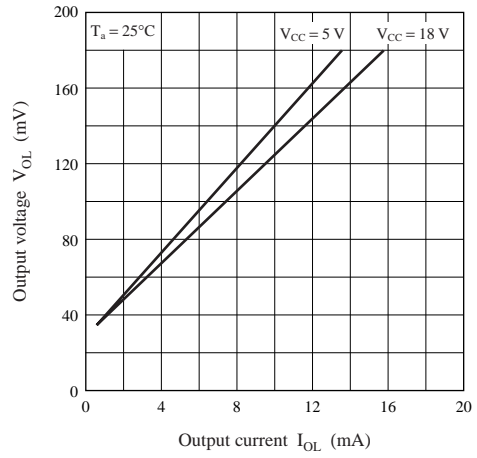


■ Main Characteristics (continued)

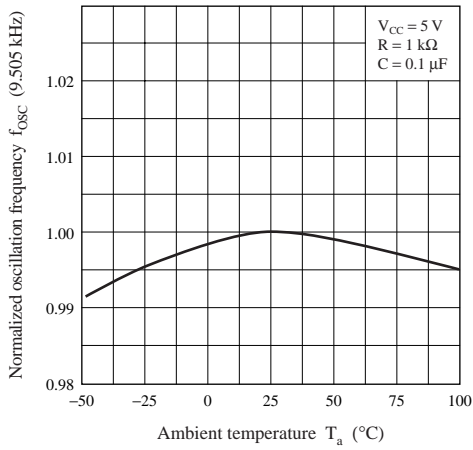
$V_{OH} - I_{OH}$



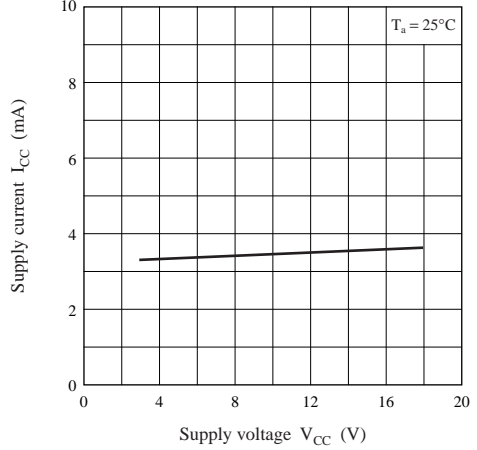
$V_{OL} - I_{OL}$



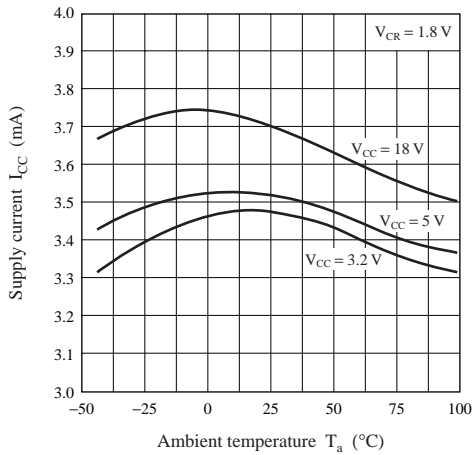
$f_{OSC} - T_a$



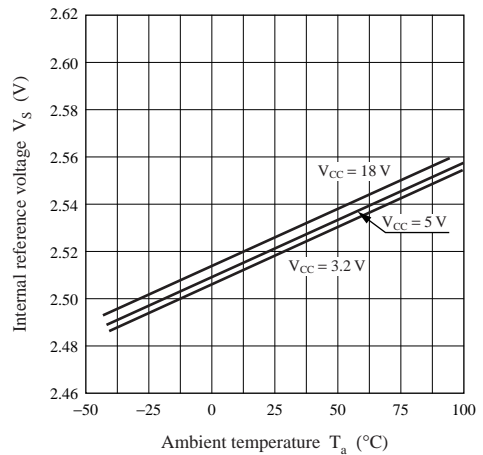
$I_{CC} - V_{CC}$



$I_{CC} - T_a$



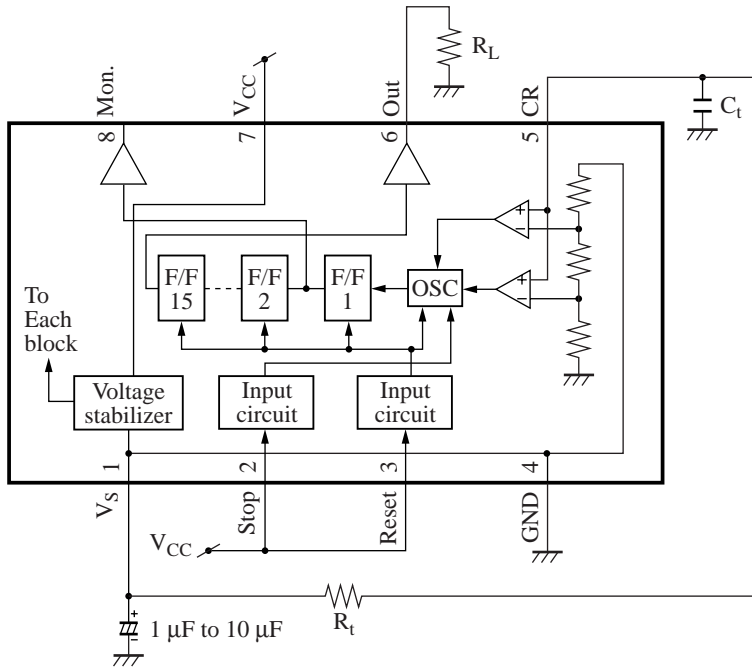
$V_S - T_a$



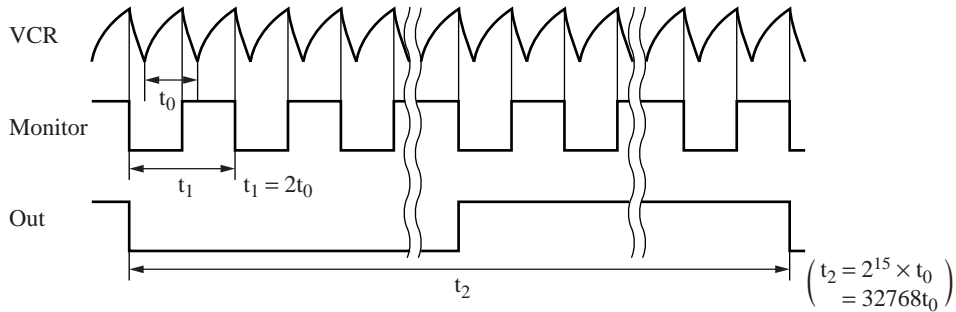
■ Application Circuit Example

The AN6783S can be used as an integrator-type timer and as a long time timer using a stop pin. It is also usable as a ultra low frequency oscillator.

1. Ultra low frequency oscillation circuit

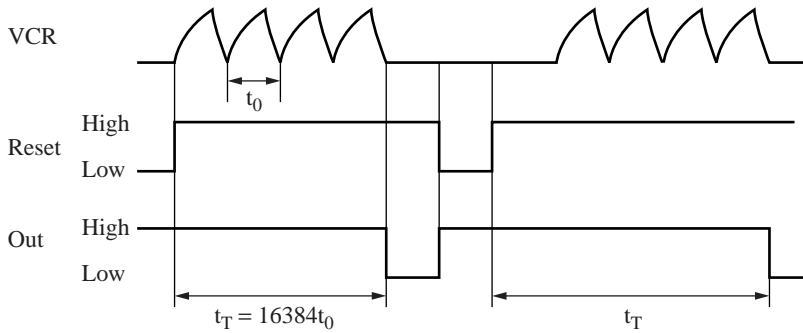
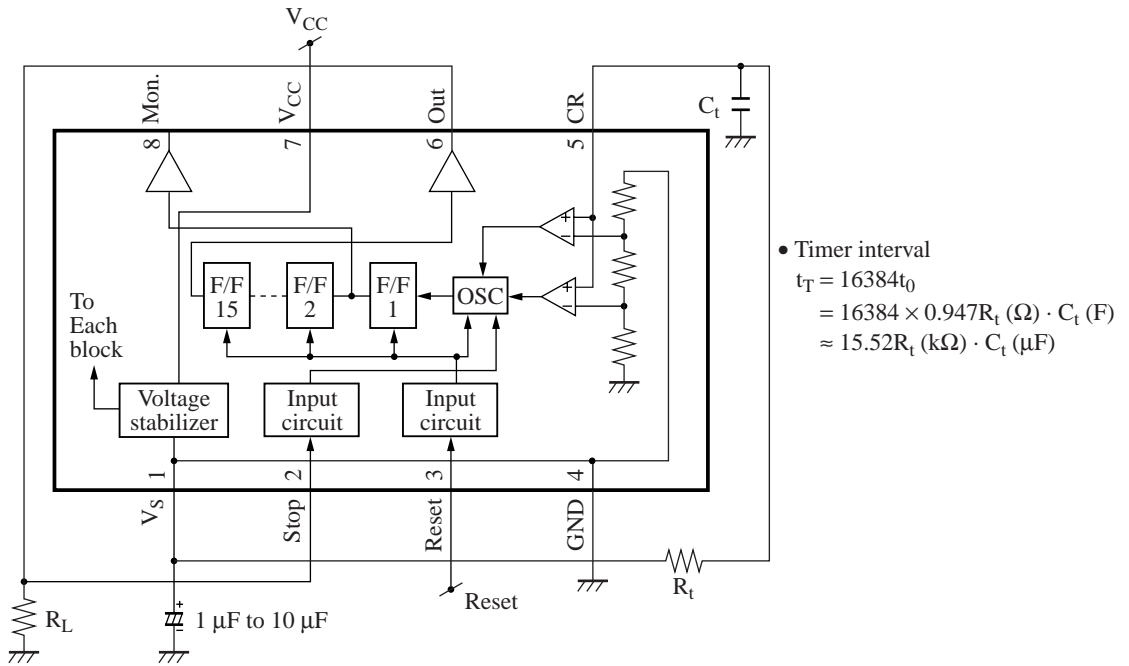


- Oscillation period
 $t_{0(S)} \approx 0.947R_t (\Omega) \cdot C_t (F)$
- Output period
 $t_{2(S)} \approx t_0 \times 32768$
 $\approx 31.03R_t (k\Omega) \cdot C_t (\mu F)$



■ Application Circuit Example (continued)

2. Timer

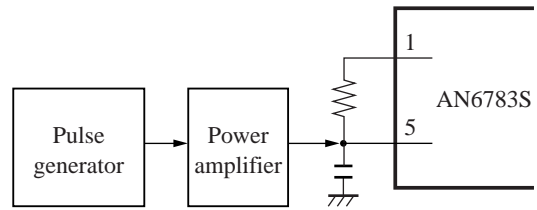


Note) For AN6784 and AN6785, above descriptions can be applied to with regard to their same pin functions.

■ Application Circuit Example (continued)

3. Quick check of the operation of the equipment which uses AN6783S

- 1) Input to pin 5 a higher frequency than the frequency with C and R, and the timer's time becomes short. Since a capacitor of big capacitance is attached at pin 5 at this moment, use a power amplifier which has an output of a totem pole type. (If the output of a pulse generator is large enough, it is not necessary.) But be careful to keep an input waveform of 1.8 V or less.
- 2) Place R_{TEST} in parallel with R_t only at the test to lower the resistance, and an oscillation frequency may be high and a timer interval can be shortened.



Oscillation waveform of pin 5

